



Hillary Cain
Dynamic Flow Marketing
415-350-4860
hcain@dynamicflow.com

Multigig Introduces Industry's Lowest Jitter Single IC Dual-PLL Programmable Synthesizer

Ultra low jitter improves performance and bit error rate in high-speed communications applications

San Jose, Calif. – July 13, 2011: [Multigig](#), the technology leader in high performance timing ICs for the wired and wireless communications markets, today announced the availability of the [MPS14](#), the industry's highest performance, dual-PLL synthesizer on a single IC. Ideal for high-speed telecommunications applications, the MPS14 offers a combination of high-performance, ultra low jitter, between 0.2ps and 0.45ps, and extremely advanced clocking functions through external FPGA logic. The MPS14 represents the first device in the Multigig Precision Synthesizer family of products.

Addressing the increased port density needs of Optical Transport Network (OTN), broadcast video and other telecommunications applications, the MPS14 integrates two ultra low jitter, any frequency, flexible PLLs that can independently generate output frequencies from 12MHz to 938MHz with precision better than 0.1ppb. In addition to high performance telecommunications applications, the MPS14 also delivers clocking capabilities that benefit applications in networking, data storage, SERDES and PHY applications. The MPS14 operates from a low-cost fundamental mode crystal in the range of 22MHz to 54MHz, eliminating the need for boutique and poorly available SAW, inverted-mesa and third overtone quartz technologies. Featuring independently programmable output buffers with independent power rails that allow mix-and-match output formats, the MPS14 supports any combination of LVPECL, LVDS, HCSL and CMOS on its 4 outputs.

Michael Canning, CEO of Multigig, Inc. emphasizes, "Multigig is enabling a whole new level of timing capability for telecommunications with this product. As a single IC dual-PLL solution, the MPS14 directly addresses the integration and port density needs of the next generation of telecom infrastructure while also providing the performance, flexibility and functionality that the industry requires."

Based on Multigig's proprietary RotaryWave™ and featuring DigiPull technology that replaces analog varactor-based crystal pulling with precise numerical frequency tuning, the MPS14 can operate as a highly-configurable jitter attenuator when controlled using an FPGA through an SPI

bus. Using the FPGA a customer can scale inputs and implement special purpose functions such as hitless switching, hold over, phase offset control and fast lock. In alternative PLL devices, these functions, if available, are typically hard coded and the customer cannot modify them. The MPS14 can also operate as an exact frequency translator or clock generator with ultra low jitter.

Pricing and Availability

The MPS14 7x7mm 48-pin QFN is available now for sampling. Pricing varies based on volume. Contact sales at sales@multigig.com

About Multigig, Inc.

Multigig, Inc., is a fabless semiconductor company that provides advanced next generation clock and timing solutions for the wired and wireless communications markets. Over 30 issued patents protect Multigig's proprietary technology. Multigig's corporate headquarters are located at 2645 Zanker Road, Suite 101 San Jose, CA 95134. For more information on the company and products visit Multigig online at www.multigig.com or call 408-514-1350.