

Multigig Announces RotaryWave Clock Technology

Revolutionary New Clock Design for High-Performance Silicon; First Major Clock Technology Advance in Decades Allows for Typical 75% Measured Power Savings, Plus Performance and Integration Across Diverse Markets; 22 Granted Patents Worldwide and 50 Pending

May 8, 2006 – Scotts Valley, CA – Multigig – developers of unique new clock technology for the high-performance silicon market – today announced the company's revolutionary new RotaryWave™ on-chip clock technology, the first major advance in semiconductor clock design in the last several decades. The RotaryWave clock enables semiconductors to achieve their maximum performance by delivering extremely precise, high-resolution, low-noise timing signals with far less power than is associated with standard clock designs (typical power savings of 75% have been measured when compared to existing solutions).

RotaryWave is a fundamental technology that works on all existing manufacturing processes - zero manufacturing changes are required to implement RotaryWave clocks into a design. RotaryWave clocks also scale well with advanced processes. Furthermore, this technology has excellent signal integrity with very robust tolerance to substrate, power supply and radiated noise.

Utilizing a unique transmission line structure with 'Mobius termination' as both a clock generation and clock distribution system, RotaryWave technology is ideally suited for products as diverse as RF/analog, mixed signal and high-speed digital. Under development in 'stealth mode' since 1999, the RotaryWave clock can be readily utilized in most high-performance semiconductor projects using standard design tools and process technologies. The company's primary business will be selling groundbreaking new ADCs and silicon timing devices that utilize the RotaryWave clock.

RotaryWave technology is already being designed into or evaluated by Tier-1 manufacturers, in products ranging from RF to mixed-signal to digital. RotaryWave clocks are ideal for markets such as mobile telecommunications, imaging and military applications where standard clock technology has proven inadequate or too power-hungry. RotaryWave is considered to be a fundamental necessity for enabling high-performance, low-power silicon with sub-picosecond accurate clock signals. The patents granted and pending deal with a wide range of fundamental technology advances associated with this breakthrough.

Multigig will be announcing new products and technology specifics in the 2nd quarter of 2006 and invites interested parties to review the RotaryWave products prior to their public launch by emailing nda@multigig.com.

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"The importance of low-power clocks cannot be overstated," said Will Strauss, Principal Analyst at Forward Concepts (Tempe, AZ). "To enable the next-generation of high-performance DSP and other architectures, it is critical to lower the power burden imposed by existing clock designs. The RotaryWave is an example of what creative thinking can do to address the issue and enable true low-power, high-performance chips using existing design methodologies and process technologies."

"The development of the RotaryWave clock represents a true rarity in electronics – a fundamentally new technique that brings exceptional benefits yet is readily implemented using today's manufacturing technology," said Haris Basit, Managing Director of Multigig. "All of the products currently under development using this technology are expected to break new ground in terms of performance, functionality and low power dissipation."

Multigig, Inc. is a startup company based in Scotts Valley, CA that has developed a fundamental core advance in semiconductor clock technology, the 'heartbeat' of high-performance chip designs. Multigig has 22 patents granted worldwide and 50 pending. Further details may be found at www.multigig.com.

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